

**DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN
APPLICATION DATA SHEET (37 CFR 1.76)**

Title of Invention HIGH SPEED DATA ACCESS MEMORY ARRAYS

As the below named inventor(s), I/we declare that:

This declaration is directed to:

- The attached application, or
 Application No. 10/618,564, filed on July 10, 2003,
 as amended on _____ (if applicable);

I/we believe that I/we am/are the original and first inventor(s) of the subject matter which is claimed and for which a patent is sought;

I/we have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;

I/we acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me/us to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.

All statements made herein of my/our own knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.

FULL NAME OF INVENTOR(S)

Inventor 1 Luigi Di Gregorio

Date: 7-23-03

Signature: Luigi Di Gregorio

Citizen of: Belgium

Inventor 2 _____

Date: _____

Signature: _____

Citizen of: _____

Inventor 3 _____

Date: _____

Signature: _____

Citizen of: _____

Inventor 4 _____

Date: _____

Signature: _____

Citizen of: _____

Additional inventors are being named on additional form(s) attached hereto.

This collection of information is required by 35 U.S.C. 115 and 37 CFR 1.63. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 minute to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PA 3317606 v1



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PTO/SB/81 (02-01)

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POWER OF ATTORNEY OR AUTHORIZATION OF AGENT

Application Number	10/618,564
Filing Date	July 10, 2003
First Named Inventor	Luigi DiGregorio
Title	High Speed Data Access Memory Arrays
Group Art Unit	
Examiner Name	
Attorney Docket Number	021111-000800US

I hereby appoint:

Practitioners at Customer Number
OR

Practitioner(s) named below:

20350

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as my/our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

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I am the:

Applicant/Inventor.
 Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).

SIGNATURE of Applicant or Assignee of Record

Name Howard G. Sachs, President and CEO

Signature Howard G. Sachs

Date July 23, 2003

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required.
Submit multiple forms if more than one signature is required, see below*.

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PA 3319427 v1

JUN 19 2003 STATEMENT UNDER 37 CFR 3.73(b)

Applicant/Patent Owner: Luigi DiGregorio

Application No./Patent No.: 107618,564 Filed/Issue Date: July 10, 2003

Entitled: HIGH SPEED DATA ACCESS MEMORY ARRAYS

Telairity Semiconductor, Inc. a Corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. the assignee of the entire right, title, and interest; or
2. an assignee of an undivided part interest

In the patent application/patent identified above by virtue of either:

- A. An assignment from the inventors of the patent application/patent identified above. The assignment was recorded in the Patent and Trademark Office at Reel 013877, Frame 0415, or for which a copy thereof is attached.

OR

- B. A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

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2. From: _____ To: _____
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Additional documents in the chain of title are listed on a supplemental sheet.

Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.8]

The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

July 23, 2003
Date


Signature

Howard G. Sachs

Typed or printed name

President and CEO

Title

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.